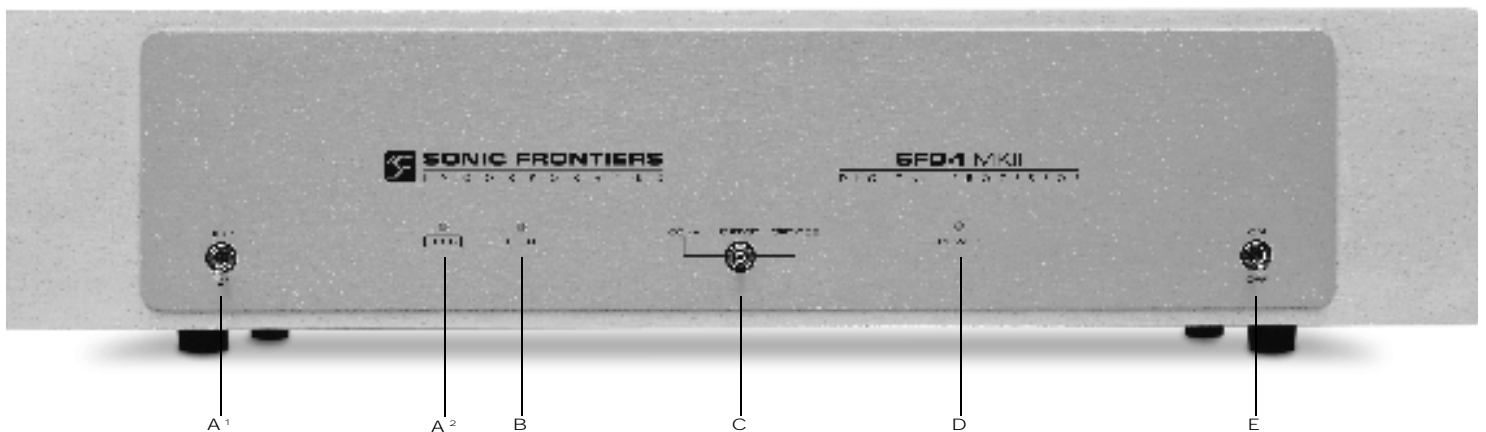


OWNER'S MANUAL UPDATE

THE SONIC FRONTIERS SFD-1 MKII DIGITAL PROCESSOR



A¹ - Phase Control Switch - This toggle switch allows the user to invert the system phasing easily and safely. When the switch is in the 0° position the SFD-1 MKII will put out a normal or non-inverted signal in relation to the signal being received from the source. When the switch is in the 180° position the SFD-1 MKII will put out an inverted version of the signal as compared to that being received from the source. This feature offers a convenient method of correcting source material which was recorded "out of absolute phase" or to counteract for a component in your system such as a preamplifier, power amplifier or signal connection which inverts phase.

A² - HDCD® - This LED will light when an HDCD (High Definition Compatible Digital) encoded digital track is being played. The HDCD decoding is switched in automatically by the SFD-1 MKII circuitry, and no user adjustments are necessary.

B - Signal Locking Indicator LED - This LED will light when an operational digital source is selected.

NOTE: Depending on the source or transport being used in conjunction with the SFD-1 MKII, it may be necessary to have the source unit in the "play" mode and the SFD-1 MKII receiving the digital transmission before the LED will light.

C - Input Selector Switch - This 3 position toggle switch selects between the three digital inputs located on the back of the SFD-1 MKII: Toslink (Plastic Fibre) Optical, RCA Coaxial Cable and ST (Glass Fibre) Optical. A connection to all 3 inputs may be made from 3 separate sources. The Signal Locking Indicator LED (B) will light when the source is activated in conjunction with the input chosen by this switch.

D - Power Indicator LED - This LED will light when the Power Switch (E) is in the ON position and the SFD-1 MKII is receiving power.

E - Power Switch - This toggle switch, when switched to the ON position, allows the SFD-1 to receive power, rendering the SFD-1 MKII operational as indicated by the Power Indicator LED (D). When the switch is in the OFF position the SFD-1 MKII is not receiving power and is not operational.

SFD-1 MKII TECHNICAL SPECIFICATIONS

Input Receiver	UltraAnalog AES20 AES/EBU ultra-low jitter Input Receiver
HDCD® Digital Filter/Decoder	Pacific Microsonics PMD100
D/A Converter	UltraAnalog D20400A (1 Dual)
Low Pass Analog Filter	Proprietary passive configuration
Frequency Response	5 Hz to 20 kHz \pm 0.25 dB
Intrinsic Jitter	< 40 ps (picoseconds)
Jitter Rejection	From 1 kHz and above (6 dB/octave)
"A" weighted S/N Ratio	> 105 dB (Balanced)
Crosstalk	> 105 dB @ 1 kHz > 95 dB 20 Hz to 20 kHz
THD	< 0.02 % @ 1 kHz < 0.30% 20 Hz to 20 kHz (balanced/ single-ended)
Output Voltage	Approx. 5.0 Volts (HDCD/S.E.) Approx. 10.0 Volts (HDCD/balanced) Approx. 2.5 Volts (non-HDCD/S.E.) Approx. 5.0 Volts (non-HDCD/balanced)
Output Stage	Fully balanced (for AC and DC parameters), high speed tube buffer
Digital Inputs	Coaxial (RCA), Optical (HP/ST-Type glass), Optical (Toslink)
Analog Outputs	1 pr. Single-ended RCA 1 pr. Balanced XLR
Tube Complement	2 - 6922 (E88CC)
Dimensions	19" Wide x 11" Deep x 4" High (48 cm x 28 cm x 10 cm)
Weight	21 lbs (9.5 kg) - unpacked
Warranty	5 years parts and labor 1 year on the tubes

THE HDCD PROCESS

The SFD-1 MKII includes a free HDCD-encoded sampler CD to demonstrate the HDCD process. This process is a true advancement in digital audio reproduction, providing one of the single greatest improvements in digital audio since the introduction of the CD format more than a decade ago. This sophisticated encoding/decoding system greatly reduces both additive and subtractive distortions in digital recordings, providing a resolution and freedom from distortion equal to that of analog recordings. In addition, the HDCD decoder's internal digital filter provides significant sonic improvements to non-HDCD source material as well.



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